

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1145192	((random with access with memory) microcomputer microprocessor (signal\$4 adj3 process\$5))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 07:20
L2	166572	(nonvolatile (non adj volatile)) with memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 07:21
L3	90550	1 and 2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 07:21
L4	83086	(semiconductor chip die dice electronic component ic (integrated adj circuit\$3)) and 3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 07:22
L5	45952	(board pcb substrate carrier ((printed circuit\$2 wiring mount\$3) adj3 board)) and 4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 07:23
L6	13140	wire and 5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 07:23
L7	4085	(resin seal\$3 encapsula\$5) and 6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 07:28
L8	4065	7 and (plurality plural multi multiple stack\$4 mount\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 07:29

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L9	3911	signal\$5 and 8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 07:30
L10	1948	(specific with application) and 9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 07:30